

4th International Conference on **Electrochemistry**

June 11-12, 2018 | Rome, Italy

Integration of functional liquids in solid-state electronic circuits**E Fillis Tsirakis¹, B Prasad¹, G Pfanzelt¹, M J Zachman², L F Kourkoutis^{2,3} and J Mannhart¹**¹Max Planck Institute for Solid State Research, Germany²Cornell University, USA³Kavli Institute at Cornell for Nanoscale Science - Cornell University, USA

Field-effect gating with solid dielectrics is the basis of modern electronics. It is a technique that is most successfully used in integrated circuits. Here, we present our work on realizing solid-state heterostructures with fully integrated liquids, opening a brand new phase-space of materials for integrated circuits. Gating with liquid electrolytes in field-effect transistors offers clean contact-electrolyte interfaces and higher polarizations than in conventional, all-solid-state architectures. We demonstrate the fabrication of electronic devices such as capacitors and field-effect transistors with integrated, patterned aqueous-NaCl solutions, which are of equal quality or even outperform standard, bulk electrolyte devices. Our work opens a new route to the exploitation of solid-liquid interfaces in integrated functional devices.

Biography

E Fillis Tsirakis completed his studies in Max Planck Institute for Solid State Research, Germany.

e.fillis@fkf.mpg.de

Notes: